

Fig. 2

	<u>∢</u>	nalog T	Analog Test Matrix	اخِر				ļ <u>.</u>													
		H				, a	Pin2	PIN3	-	-	+	+	-	NIA		PIN12	PINIS	PINIT	+	+	NC PIN 01
Application   Property   Proper	Pen Name	-				ASIPOS	ASINEG	TEST	+	-	-	+	-	8	-	9	GND	3	+	+	00,
Control   Cont	Pin_Name_Analog	-												_						-	
1	Pin_Name_Digital																				
Secondary   Seco	Pin_Name_Test_Mod	9																			
1.	Signal_Type_Digital					8		8	_	-	_			٥	0						
Section   The Name of Section   The Name o	Signal Type Analog					۵	9						o				_				
1.2.2   Par Name Code   10.1 (20.1)   10.1	Sequencer	Sec Sec	_		etup-																
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1-12		25.	2 0	Name GND		00//0001	T	00 / 601	/\@01 00/	ł		202 00/00	8	T	00/06/00	00/100	1	2 10001		0000	1,0001
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1   10   10   10   10   10   10   10	۱	3.2		1	#02,#07,#	109 300		PG11 ** P.	S	- BGI	194 - Z	P612	NC	PG21	PG2:		-1				\$
1   100	Transmitter Tori			-	The second			-		134212	Comment of the							7			
Sequence   QC	Method 85P01 6.	15	151	ŧ	101 #08	300		PG1	31 - F PG1	P64	PG1	19d	NC	P62.	PG2	PG21	1	1			48
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AW1         #W1         AW01         AW11           Sequencen         @01         force value         -100uA         to be transferred to know-how-library.           Sequencen         @02         force value         -100uA         3           Sequencen         @02         force value         100uA         4           Sequencen         @03         force value         100uA         5           Sequencen         @04         force value         100uA         7           Sequencen         @04         force value         100uA         7           Remarks:         Remarks:         10cre value         2V         7           Sequencen         @04         force value         100uA         7           Sequencen         @04         force value         100uA         7           Sequencen         @04         force value         2V         7           Sequencen         @04         force value         100uA         7           Sequencen         @04         force value         100uA         7           Sequencen         @04         force value         100uA         7           Sequencen         @04         10cce value         10cce valu	Recoiver Test		***************************************					135		1.5			1.6	Section Section		-		And in the case of the case	- Constitution	-	-
VII = DV   Sequencen (B01   force value -100uA   to be transformed to know-how-library.   Sequencen (B01   1)   Clamp   2V   Sequencen (B01   1)   Clamp   4V   Sequencen (B02   clamp   4V   5)   Clamp   4V   Sequencen (B03   clamp   2V   5)   Clamp   4V   Sequencen (B04   clamp   2V   5)   Clamp   4V   Sequencen (B04   clamp   2V   5)   Clamp   4V   Sequencen (B04   clamp   4V   5)   Clamp   4V   Clamp	Voltage Relation:	ship ( Jour	1 1		04 AW1	AWO1		AWI													
VI_=0V         Sequencen         (601         force value         -100uA         to be transforred to know-how-library.           VII=5.4V         Sequencen         (602         force value         -100uA         Sequencen         (601         1)           Clamp=5.4V         Sequencen         (602         force value         -100uA         5         4         5           Clamp=5.4V         Sequencen         (603         force value         100uA         6         6         6         6         6         6         6         6         6         6         6         6         6         6         7 <t< th=""><th></th><th>+</th><th></th><th></th><th></th><th></th><th></th><th>+</th><th>-</th><th>-</th><th>-</th><th></th><th></th><th></th><th></th><th></th><th></th><th><math>\dagger</math></th><th></th><th></th><th></th></t<>		+						+	-	-	-							$\dagger$			
VI.=0V         Sequencen         (90.1)         force value         -100uA         to be transferred to know-how-library.           VIH-5.4V         Sequencen         (90.2)         force value         -100uA         3           Clamp=5.4V         Glamp         -4V         4         5           Clamp=5.4V         Force value         -100uA         5           Clamp=5.4V         4V         5           Sequencen         (90.2)         force value         100uA         5           Clamp         2V         7)         7)           Remarks:         Remarks:         Remarks:         Remarks:         Remarks:           Clamp         4V         AV         Remarks:         Remarks:           Clamp         4V         AV         AV           Clamp         4V         AV         AV           Clamp         4V         AV         AV           Clamp         4V         AV         AV           Clamp         AV         AV         AV           Clamp         AV         AV         AV           Clamp         AV         AV         AV           Clamp         AV         AV																				-	
Clamp=5.4V   Sequencen (201   1)   1   1   1   1   1   1   1   1	Power Group P.	_	AG.		Sequenc	en (801	릚	-100uA	to be	transferred to	5 know-how-lib	1									
Clampa's AV   Sequencen   QOZ   Corce value   100uA   4V   4   5		₹	1=5.4V				T	-2v	Sedi	neucen (@01	= =		physical	-				1			
Clampa5,4V   Clampa5,4V   Clampa5,4V   Clampa5,4V   Clampa5,4V   Clampa5,4V   Clampa	ă	32 · IOH	4=15mA		Sequenc	en (@02		100uA			3)	measu	e value								
force value 100uA 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6		ê	mp=5.4V					<b>}</b>			4	release	value								
damp 2V 7) Glamp 2V 7) Glamp 4V Glamp 4V Glamp 4V Glamp 4V Glamp 4V Glamp 4V Glamp 6V Glamp 6V Glamp 6V Glamp 6V Glamp 6V Glamp 7V Glamp 7V Glamp 6V Glamp 7V Glamp 7V Glamp 6V Glamp 7V Glamp 7		+	$\dagger$		Continuo	- CO		4000		-	ro (d	openo	onnection	+	$\prod$		$\dagger$	+		$\dagger$	
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	Ana	Analog Test Matrix	Matrix	PIN1	PIN2	PIN3	PIN4	SNId	PIN6	PIN7	PIN8	PIN9			
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				· >	>	9	۵	: <b>ɔ</b>			۵.	. ~			
			Name in Test Mode=							GOASLEEP		CLK1M			
Sequencer	Spec	time sequence							Γ			•			
	1:0		SleepMode Sup:		The second second							and the second			
					4.5V										
			Setupname	10\	±5mV	1uF	20	5.25V		I	oben	ı			
	1.2	-	LoadMin					uedo							
			measure												
			low limit					4.75V							
			high limit					5.25V							
_															
	1.3	2	LoadMax1												
			measure												
			low limit					4.75V							
			high limit					5.25V							
	1.4	3	LoadMax2										-		
			measure												
			low limit					4.75V							
			high limit					5.25V							
	2.0		WakeUp Circuit		/ 50		* - * * * * * * * * * * * * * * * * * *	The state of the s							
	2.1			100	_	ų.	>	5 257		ī	uado		NAME OF TAXABLE PARTY.		
	2.2	-	TestMode				pattern for	Run pattern for analog Test mode	1				Com	CommandButton()	
	2.3	2	IDDsleep				Ĺ	* <20uA		pulse 1us					
						5.3V									
	2.4	က	Sup6Off			* 100uA±50uA									
	2.5	4	Sup5Off				4.5V		_	~	8V * <1uA				

Fig. 4

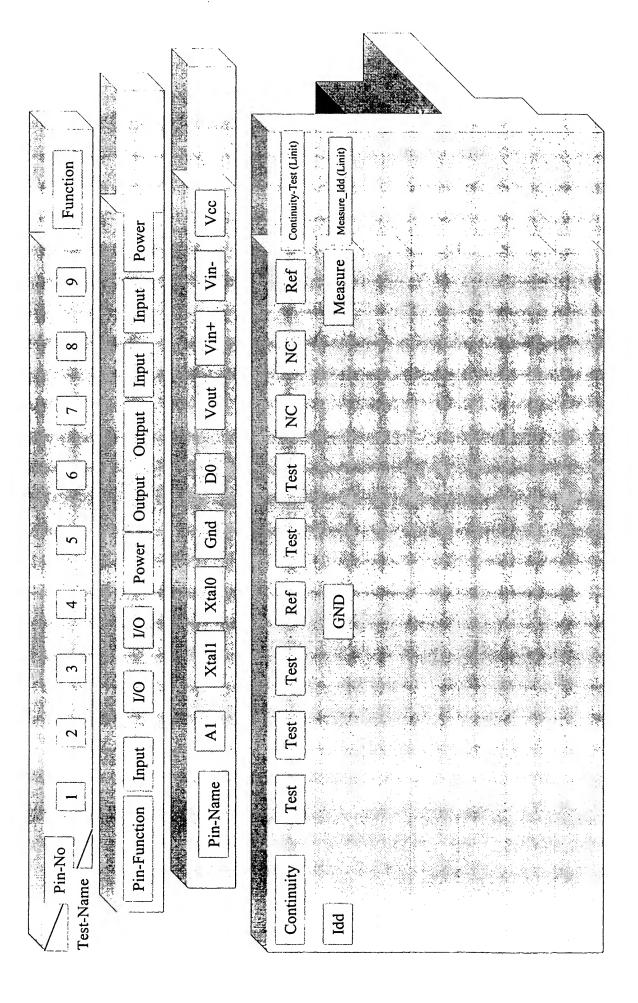


Fig. 5

